

WEST Search History

DATE: Saturday, June 25, 2005

| Hide? | Set Name | Query | Hit Count |
|--------------------------|----------|----------------------------------|-----------|
| | | <i>DB=USPT; PLUR=YES; OP=ADJ</i> | |
| <input type="checkbox"/> | L8 | L7 and stage | 7 |
| <input type="checkbox"/> | L7 | L5 and series\$ | 15 |
| <input type="checkbox"/> | L6 | L5 and (preread or pre-read) | 0 |
| <input type="checkbox"/> | L5 | L4 and (program\$ or writ\$) | 15 |
| <input type="checkbox"/> | L4 | L3 and switch\$ adj4 resistor | 18 |
| <input type="checkbox"/> | L3 | L2 and resistance | 191 |
| <input type="checkbox"/> | L2 | L1 and poly\$ adj3 resistor | 222 |
| <input type="checkbox"/> | L1 | memory and cell | 123874 |

VIET Q. NGUYEN
PRIMARY EXAMINER

END OF SEARCH HISTORY